

# MMJT9410

Preferred Device

## Bipolar Power Transistors

### NPN Silicon

#### Features

- Collector –Emitter Sustaining Voltage –  
 $V_{CE(sus)} = 30 \text{ Vdc (Min) @ } I_C = 10 \text{ mAdc}$
- High DC Current Gain –  
 $h_{FE} = 85 \text{ (Min) @ } I_C = 0.8 \text{ Adc}$   
 $= 60 \text{ (Min) @ } I_C = 3.0 \text{ Adc}$
- Low Collector –Emitter Saturation Voltage –  
 $V_{CE(sat)} = 0.2 \text{ Vdc (Max) @ } I_C = 1.2 \text{ Adc}$   
 $= 0.45 \text{ Vdc (Max) @ } I_C = 3.0 \text{ Adc}$
- SOT–223 Surface Mount Packaging
- Epoxy Meets UL 94 V–0 @ 0.125 in
- ESD Ratings: Human Body Model, 3B; > 8000 V  
Machine Model, C; > 400 V
- Pb–Free Package is Available

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector –Emitter Voltage	$V_{CEO}$	30	Vdc
Collector –Base Voltage	$V_{CB}$	45	Vdc
Emitter –Base Voltage	$V_{EB}$	$\pm 6.0$	Vdc
Base Current – Continuous	$I_B$	1.0	Adc
Collector Current – Continuous – Peak	$I_C$	3.0 5.0	Adc
Total Power Dissipation @ $T_C = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$ Total $P_D$ @ $T_A = 25^\circ\text{C}$ mounted on 1" sq. (645 sq. mm) Collector pad on FR–4 bd material Total $P_D$ @ $T_A = 25^\circ\text{C}$ mounted on 0.012" sq. (7.6 sq. mm) Collector pad on FR–4 bd material	$P_D$	3.0 24 1.7 0.75	W mW/°C W
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	–55 to +150	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction–to–Case	$R_{\theta JC}$	42	°C/W
Thermal Resistance, Junction–to–Ambient on 1" sq. (645 sq. mm) Collector pad on FR–4 bd material	$R_{\theta JA}$	75	°C/W
Thermal Resistance, Junction–to–Ambient on 0.012" sq. (7.6 sq. mm) Collector pad on FR–4 bd material	$R_{\theta JA}$	165	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 5 seconds	$T_L$	260	°C

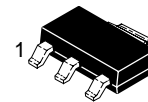
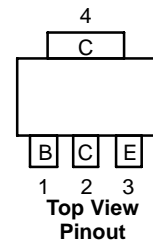
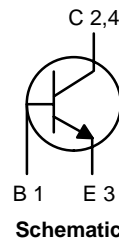
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



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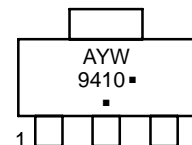
<http://onsemi.com>

**POWER BJT**  
 $I_C = 3.0 \text{ AMPERES}$   
 $BV_{CEO} = 30 \text{ VOLTS}$   
 $V_{CE(sat)} = 0.2 \text{ VOLTS}$



SOT–223 (TO–261)  
CASE 318E  
STYLE 1

#### MARKING DIAGRAM



A = Assembly Location  
Y = Year  
W = Work Week  
9410 = Device Code  
▪ = Pb–Free Package

(Note: Microdot may be in either location)

#### ORDERING INFORMATION

Device	Package	Shipping†
MMJT9410	SOT–223	1000 / Tape & Reel
MMJT9410G	SOT–223 (Pb–Free)	1000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

**Preferred** devices are recommended choices for future use and best overall value.

# MMJT9410

## ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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### OFF CHARACTERISTICS

Collector–Emitter Sustaining Voltage (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = 0 Adc)	V <sub>CEO(sus)</sub>	30	–	–	Vdc
Emitter–Base Voltage (I <sub>E</sub> = 50 μAdc, I <sub>C</sub> = 0 Adc)	V <sub>EBO</sub>	6.0	–	–	Vdc
Collector Cutoff Current (V <sub>CE</sub> = 25 Vdc, R <sub>BE</sub> = 200 Ω) (V <sub>CE</sub> = 25 Vdc, R <sub>BE</sub> = 200 Ω, T <sub>J</sub> = 125°C)	I <sub>CER</sub>	–	–	20 200	μAdc
Emitter Cutoff Current (V <sub>BE</sub> = 5.0 Vdc)	I <sub>EBO</sub>	–	–	10	μAdc

### ON CHARACTERISTICS (Note 1)

Collector–Emitter Saturation Voltage (I <sub>C</sub> = 0.8 Adc, I <sub>B</sub> = 20 mAdc) (I <sub>C</sub> = 1.2 Adc, I <sub>B</sub> = 20 mAdc) (I <sub>C</sub> = 3.0 Adc, I <sub>B</sub> = 0.3 Adc)	V <sub>CE(sat)</sub>	–	0.105 0.150 –	0.150 0.200 0.450	Vdc
Base–Emitter Saturation Voltage (I <sub>C</sub> = 3.0 Adc, I <sub>B</sub> = 0.3 Adc)	V <sub>BE(sat)</sub>	–	–	1.25	Vdc
Base–Emitter On Voltage (I <sub>C</sub> = 1.2 Adc, V <sub>CE</sub> = 4.0 Vdc)	V <sub>BE(on)</sub>	–	–	1.10	Vdc
DC Current Gain (I <sub>C</sub> = 0.8 Adc, V <sub>CE</sub> = 1.0 Vdc) (I <sub>C</sub> = 1.2 Adc, V <sub>CE</sub> = 1.0 Vdc) (I <sub>C</sub> = 3.0 Adc, V <sub>CE</sub> = 1.0 Vdc)	h <sub>FE</sub>	85 80 60	200 – –	– – –	–

### DYNAMIC CHARACTERISTICS

Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0 Adc, f = 1.0 MHz)	C <sub>ob</sub>	–	85	135	pF
Input Capacitance (V <sub>EB</sub> = 8.0 Vdc)	C <sub>ib</sub>	–	200	–	pF
Current–Gain – Bandwidth Product (Note 2) (I <sub>C</sub> = 500 mA, V <sub>CE</sub> = 10 Vdc, F <sub>test</sub> = 1.0 MHz)	f <sub>T</sub>	–	72	–	MHz

1. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
2. f<sub>T</sub> = |h<sub>FE</sub>| • f<sub>test</sub>

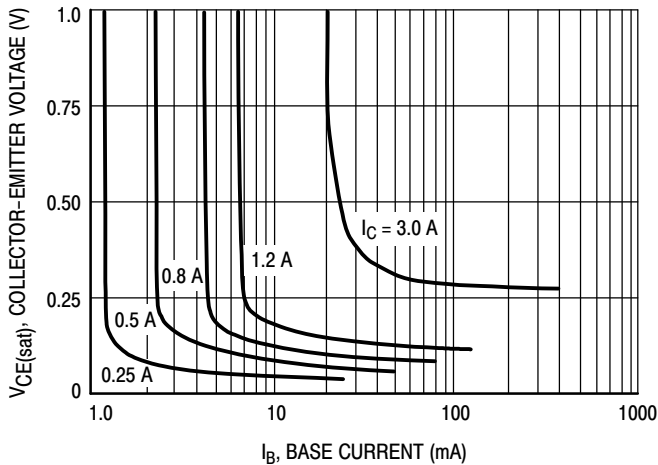


Figure 1. Collector Saturation Region

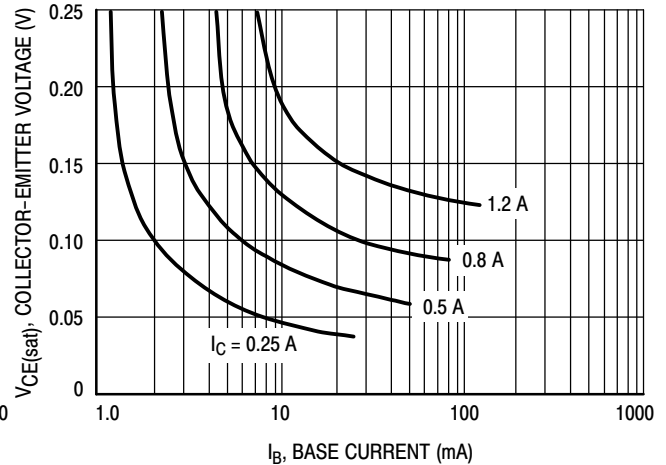


Figure 2. Collector Saturation Region

# MMJT9410

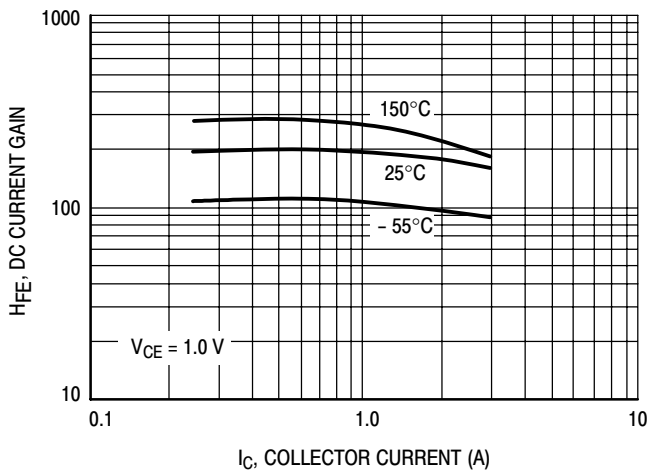


Figure 3. DC Current Gain

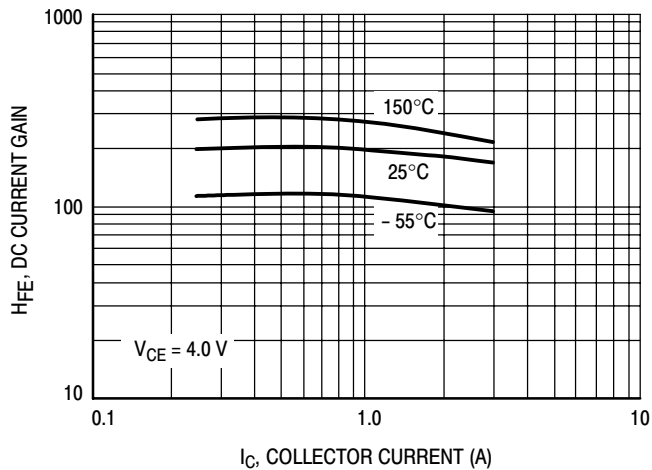


Figure 4. DC Current Gain

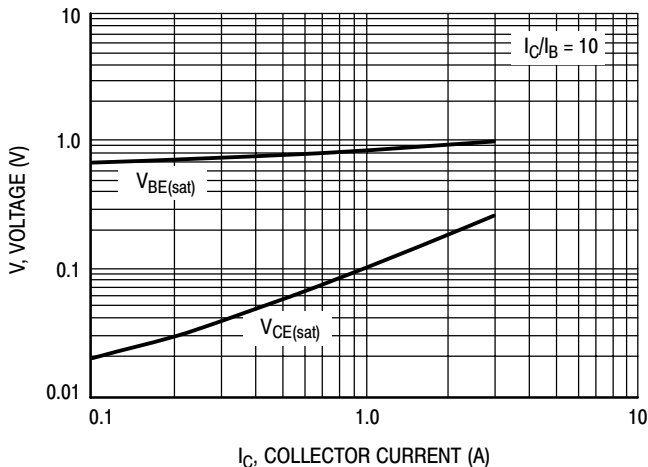


Figure 5. "On" Voltages

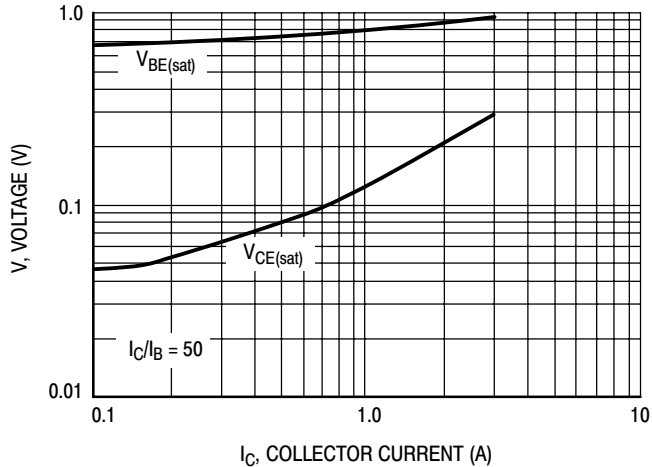


Figure 6. "On" Voltages

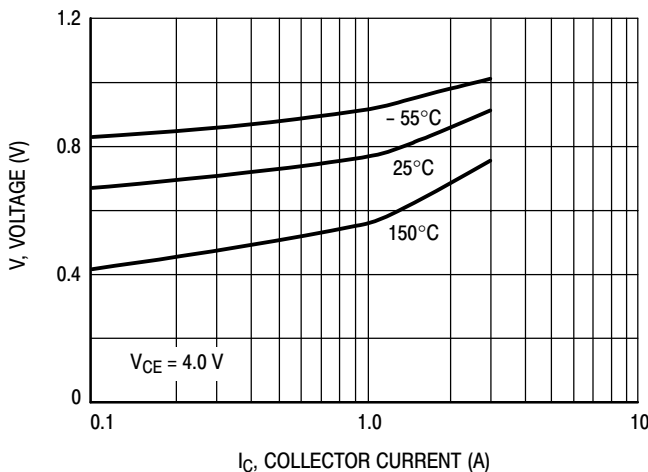


Figure 7.  $V_{BE(on)}$  Voltage

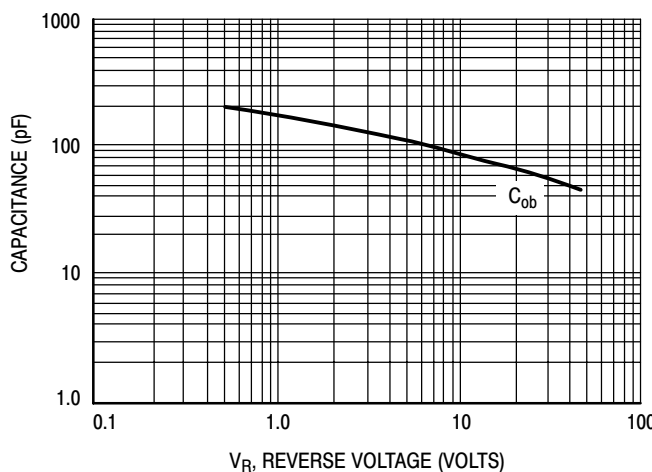


Figure 8. Capacitance

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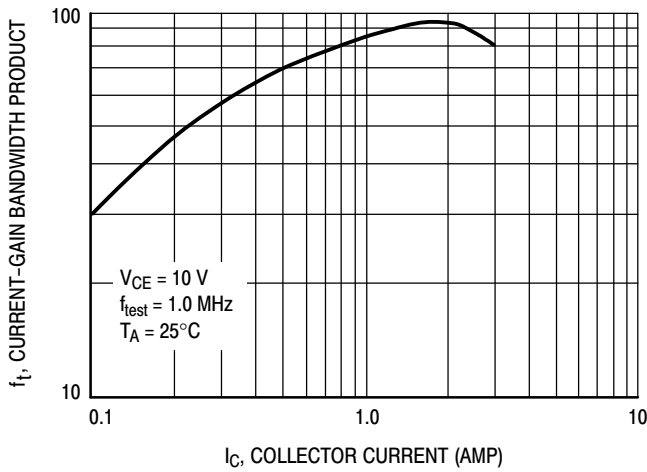


Figure 9. Current-Gain Bandwidth Product

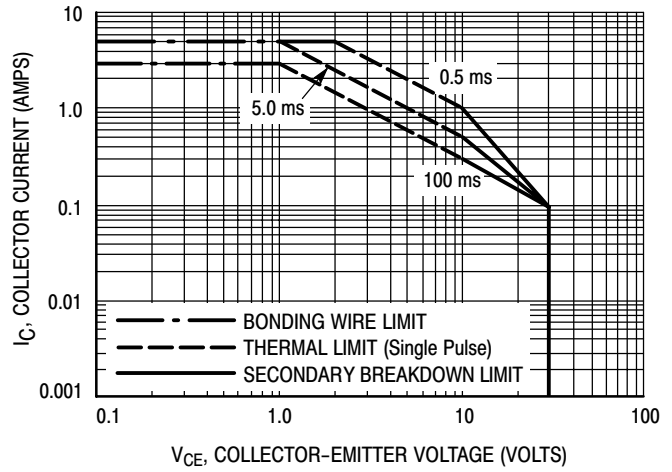


Figure 10. Active Region Safe Operating Area

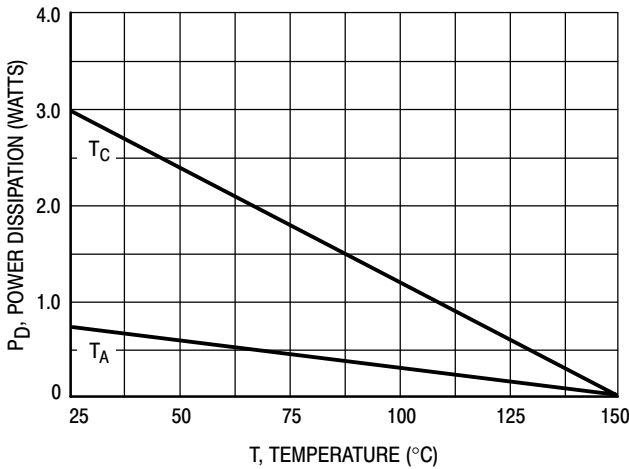


Figure 11. Power Derating

There are two limitations on the power handling ability of a transistor: average junction temperature and secondary breakdown. Safe operating area curves indicate  $I_C - V_{CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 10 is based on  $T_{J(pk)} = 150^\circ\text{C}$ ;  $T_C$  is variable depending on conditions. Secondary breakdown pulse limits are valid for duty cycles to 10% provided  $T_{J(pk)} \leq 150^\circ\text{C}$ .  $T_{J(pk)}$  may be calculated from the data in Figure 12. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by secondary breakdown.

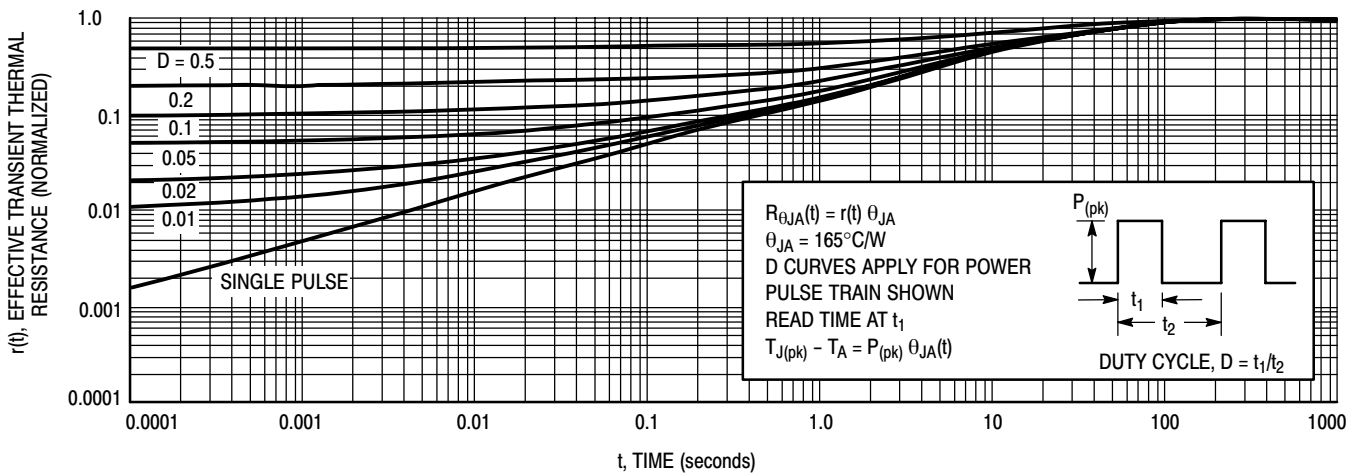
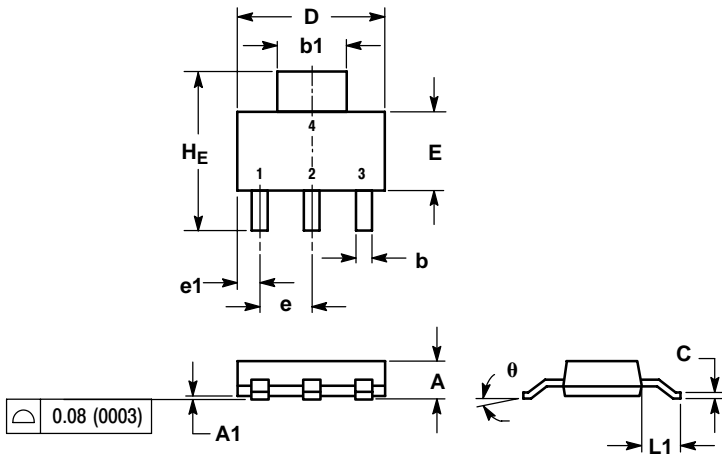


Figure 12. Thermal Response

# MMJT9410

## PACKAGE DIMENSIONS

SOT-223 (TO-261)  
CASE 318E-04  
ISSUE L



NOTES:

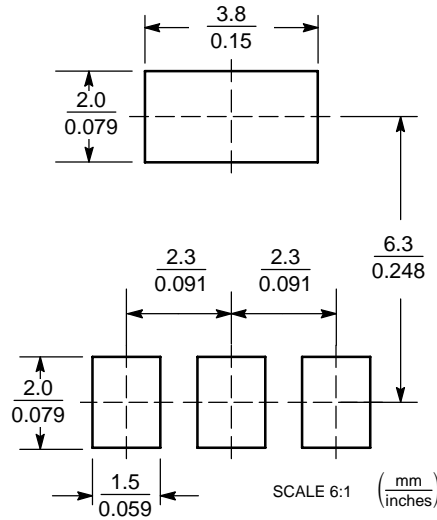
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.50	1.63	1.75	0.060	0.064	0.068
A1	0.02	0.06	0.10	0.001	0.002	0.004
b	0.60	0.75	0.89	0.024	0.030	0.035
b1	2.90	3.06	3.20	0.115	0.121	0.126
c	0.24	0.29	0.35	0.009	0.012	0.014
D	6.30	6.50	6.70	0.249	0.256	0.263
E	3.30	3.50	3.70	0.130	0.138	0.145
e	2.20	2.30	2.40	0.087	0.091	0.094
e1	0.85	0.94	1.05	0.033	0.037	0.041
L1	1.50	1.75	2.00	0.060	0.069	0.078
HE	6.70	7.00	7.30	0.264	0.276	0.287
θ	0°	-	10°	0°	-	10°

STYLE 1:

- PIN 1. BASE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

### SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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